

PART B - FEE(S) TRANSMITTAL 03-03-05

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27572 7590 12/17/2004

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G. Gregory Schivley		(Depositor's name)
<i>G. Gregory Schivley</i>		(Signature)
March 2, 2005		(Date)

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. BOX 828
BLOOMFIELD HILLS, MI 48303

03/07/2005 AWONDAF2 00000103 503213 10647063

01 FC:1501	1400.00 DA
02 FC:1504	300.00 DA
03 FC:8001	15.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/647,063	08/22/2003	Tsuyoshi Yoneyama	9319S-000525	9711

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1400	\$300	\$1700	03/17/2005
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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NGUYEN, TUAN T	2824	365-189050
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1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 Harness, Dickey & Pierce, P.L.C.
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(A) NAME OF ASSIGNEE

Seiko Epson Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY) AWONDAF2 00000102 503213 10647063

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 b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature *G. Gregory Schivley*

Date March 2, 2005

Typed or printed name *G. Gregory Schivley*

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